

101#22

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10032498	01/02/2002	716	11	2825	L/N

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****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**
JAPAN 2001-169010 06/05/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <i>yes</i>		ATTORNEY DOCKET NO
35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <i>yes</i>		108066-00047
Verified and Acknowledged Examiners's initials		
TITLE : LSI design method having dummy pattern generation process and LCR extraction process and computer program therefor		

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for 0.0
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
			Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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